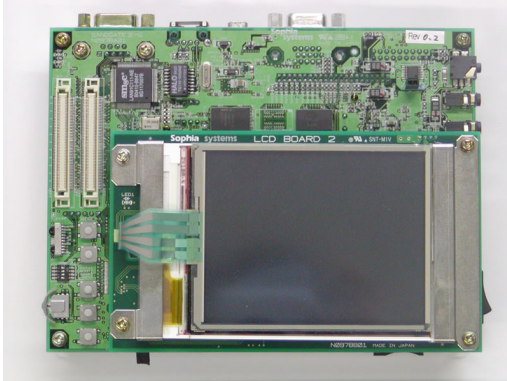




Sandgate II - G (BDT017) (BDT018)



Sandgate II, Evaluation Board with the Marvell® PXA270 processor and the Marvell® 2700G multimedia accelerator includes the peripherals useful for the development of PDA/Hand-held equipments with multimedia applications. And can be used as an excellent design base for the actual system development.

This reference board enables the engineer to develop the software simultaneously with the hardware development, which can expedite the overall system development period.

* The combination of this board and Sophia's emulator provides on-chip debugging.

*Note: Exact appearance may differ.

Main Hardware Features

- CPU: Marvell® PXA270
- Memory: 64 Mbytes Flash,
64 or 128 Mbytes SDRAM*
(*64 Mbytes SDRAM for BDT017
*128 Mbytes SDRAM for BDT018)
- Marvell® 2700G Multimedia Accelerator
- I/O IrDA port, RS-232C port,
USB 2.0 (Client), USB 1.1(Client),
USB 1.1(Host)
Ethernet port(10/100Base-T)
- SD/Memory Stick socket
- CF socket
- SIM socket
- LCD with touch panel (In color with 640 x 480)
- Analog RGB(VGA Monitor)
- IDE I/F
- IC to control the power supply
- Audio CODEC
- Headset Jack
- Audio Jack(IN/OUT)
- Expansion Bus
- Dimension 165 x 125 mm
- JTAG port

Configurations

- PXA270 Evaluation Board
 - Cable for Jflash
 - Serial Cable(Cross Cable)
 - AC Adapter
 - CD-ROM including:
- Stylus for Touch panel
 - CF Memory Card
 - LAN Cable(Cross Cable)

Design Information- Schematic(PDF), Parts List, CPLD data in source and binary, BSP Installation Guide, User's manual

Software- JFlash in source and binary, POST(Power On Self Test)in source and binary, BSP(Board Support Package) for Windows CE 5.0 (Included) BSP(Board Support Package) for Linux. (Option)

Source code files for Windows CE.5.0 BSP

- Kernel: OAL part
- Boot Loader (EBOOT)
- 2700G display driver
- Touch panel driver
- Audio driver
- Ethernet driver (for debugging)
- Serial driver (for debugging): FFUART)+IrDA(STUART)
- USB driver (USB1.1 Client, USB1.1 Host)
- CF driver
- SD card driver

* Note1: This BSP is initially setup to support the following.

- ROM: 64Mbytes - RAM: 64Mbytes
- Frequency: 520 MHz
- Suspend function and Resume function are not supported.

* Note 2: POST is written into the board on delivery.

The optional installation service of Windows CE is available.

* Note 3: Use the software under the described product conditions.

Source code files for Linux BSP (Option)

- Linux kernel 2.6.9
- Boot Loader (Blob)
- Ethernet
- LEDs
- USB 1.1 (Host)
- VGA
- IrDA
- AC97(sound)
- 2700G
- MTD (Flash Memory Support)
- Compact Flash

* Note 1: Use the software under the described product conditions.

* Note 2: There is a fee for the Linux BSP package.

* Note 3: The optional installation service of Linux is available.

Source code is provided as example code and can not be used "as is" for production systems.
All brands and product names are trademarks or register trademarks of their respective companies.
All configurations are subject to change without notice.

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Product Code: Product Name:

BDT017 PXA270/2700G EB/SG2G

BDT018 PXA270/2700G EB/SG2G-128

(Option: U60403: SG2+/G Linux BSP)