



Sandgate II - Plus (BDT012S) (BDT013S)



*Note: Exact appearance may differ.

Sandgate II, Evaluation Board with Marvell® PXA270 processor includes the peripherals useful for the development of PDA/Hand-held equipments.

It is an excellent design base for the actual system development.

This reference board enables the engineer to develop the software simultaneously with the hardware development, which can expedite the overall system development period.

* The combination of this board and Sophia's emulator provides on-chip debugging.

Main Hardware Features

- CPU: Marvell® PXA270
- Memory: 64 Mbytes Flash,
64 or 128 Mbytes SDRAM*
(*64 Mbytes SDRAM for BDT012S
*128 Mbytes SDRAM for BDT013S)
- I/O IrDA port, RS-232C port,
USB 2.0 (Client), USB 1.1(Client),
USB 1.1(Host)
Ethernet port (10/100Base-T)
- SD/Memory Stick socket
- CF socket
- SIM socket
- LCD with touch panel (In color with 640 x 480)
- IC to control power supply
- Audio CODEC
- Headset Jack
- Audio Jack (IN/OUT)
- Reset switch
- Expansion Bus
- Dimension 165 x 125 mm
- JTAG port

Configurations

- PXA270 Evaluation Board
 - Cable for Jflash
 - Serial Cable (Cross Cable)
 - AC Adapter
 - CD-ROM including:
- Stylus for Touch panel
 - CF Memory Card
 - LAN Cable (Cross Cable)

Design Information- Schematic, Parts List, CPLD data in source and binary, BSP Install Guide, User's manual

Software- JFlash in source and binary, POST (Power On Self Test) in source and binary, BSP (Board Support Package) for Windows CE 5.0/6.0 BSP (Board Support Package) for Linux

Source code files for Windows CE.5.0/6.0* BSP

- Kernel: OAL Part
- Boot Loader (EBOOT)
- Display driver - 640 x 480
- Touch panel driver
- Audio driver
- Ethernet driver (for debugging)
- Serial driver (for debugging)+(FFUART)+IrDA STUART)
- USB driver (USB1.1 Client, USB1.1 Host)
- CF driver
- SD card/driver

* Note1: This BSP is initially setup to support the following.

- ROM: 64 Mbytes - RAM: 64 Mbytes
- Frequency: 520MHz
- Suspend function and Resume function are not supported.

* Note2: POST is written into the board on delivery.

The optional installation service of Windows CE is available.

* Note3: Use the software under the described product conditions.

* Note4: There is a fee for the WinCE BSP 6.0 package.

Source code files for Linux BSP (included)

- Linux kernel 2.4
- Boot Loader (Red Boot)
- Ethernet
- Serial driver for debugging

Source code files for Linux BSP (Option)

- Linux kernel 2.6.9
- Boot Loader (Blob)
- Ethernet
- LEDs
- USB 1.1 (Host)
- VGA
- IrDA
- AC97(sound)
- MTD (Flash Memory Support)
- Compact Flash

* Note1: Use the software under the described product conditions.

* Note2: There is a fee for the Linux BSP package.

* Note3: The optional installation service of Linux is available.

Source code is provided as example code and can not be used "as is" for production systems.

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All configurations are subject to change without notice.

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Product Code: Product Name:

BDT012S PXA270 EVA BOARD/SG2+

BDT013S PXA270 EVA BOARD/SG2-128+

(Option: U60403: SG2+/G Linux BSP)

U60405: SG2+ WECE6 BSP)