



UniSTAC™ II/J TX49 In-Circuit Emulator



- ❑ Supports Toshiba's 64-bit microprocessor core, TX49
- ❑ Unlimited software breakpoints
- ❑ Hardware breakpoints (instruction, access, and physical address break can be specified)
- ❑ Download to Flash Memory
- ❑ Branch trace capability (512K steps)
- ❑ Watchpoint®, a powerful high-level language debugger provides a common user interface for all Sophia Systems emulators
(USB: Windows98/Me/2000/XP)
(TCP/IP: Windows98/Me/NT/2000/XP)
- ❑ USB and LAN host interfaces for high-speed communication with host computer

Specifications

Target CPU	TX49 series (4925, 4926, 4927, 4937, 4938, 4939, 4951, 4955)
Clock	Supports CPU's maximum clock speed
Target Vcc	2.5V to 3.6 V (3.3V or 2.5V output signals from ICE)
Memory and I/O	Entire space is available to user except for monitor area
Signal mask	Interrupt input or NMI input is maskable
Breakpoints and Break Options	<p>Execution address break options:</p> <ul style="list-style-type: none"> • 3 hardware breakpoints (1 instruction breakpoint, 1 access breakpoint, 1 physical address breakpoint) • Unlimited software breakpoints <p>Other break options:</p> <ul style="list-style-type: none"> • Forced break from Debug Monitor <p>* When the following function is in use, release 1 instruction breakpoint. - Step Over, Step Out, Come To</p>
Branch Trace	<p>Supports 133 MHz clock Trace memory capacity: 512K steps The following condition can be specified – 1 instruction trace trigger, 1 access trace trigger, 1 physical address trace trigger Breakpoint and trace trigger should be used exclusively. (cannot be used simultaneously)</p>
Flash Memory	Download to flash memory on the target system

Configuration

The UniSTAC- II/J TX49 provides a high level of software debugging from your PC/AT or notebook computer. It is a development tool for Toshiba's TX49 for debugging. The UniSTAC-II/J TX49 ICE can be connected to a JTAG connector on the target.

Hardware

A UniSTAC-II/J TX49 emulation system consists of the UniSTAC-II TX49 JTAG Probe Set with USB host interface or LAN interface as an option.

Software

Watchpoint®, a high-level language debugger for Windows® is included with the TX49 Probe Set.

Media: CD-ROM

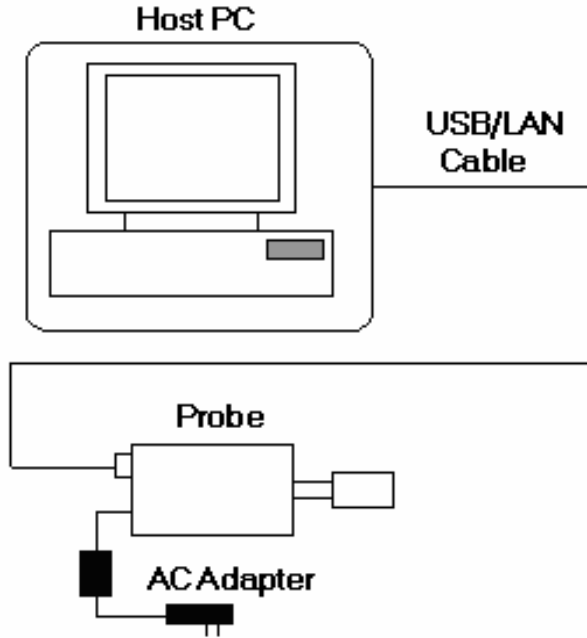
Supported Tool Chains:

Watchpoint supports the following compiler:

GreenHills GHS

Target JTAG ICE Connection

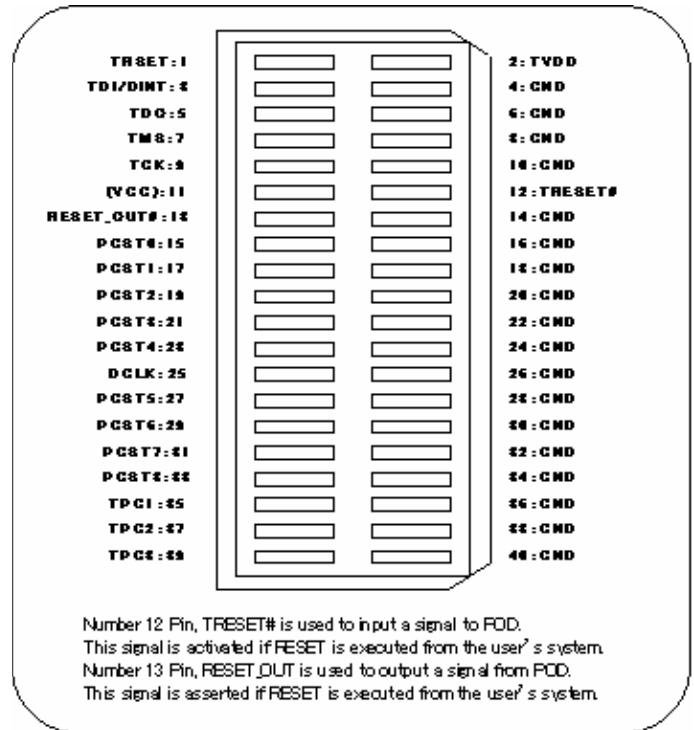
Recommended connector: FTSH-120-01-L-D (SAMTEC, INC)



UniSTAC-II TX49 JTAG ICE

System requirements for Watchpoint® Debugger:

OS: <USB>Windows 98/Me/2000/XP
<TCP/IP>Windows98/Me/NT/2000/XP
Memory: 32 Mbytes (64 Mbytes recommended)
Hard Disk: 20 Mbytes for installation



EJTAG Connector Pin Assignment(Top view)

Ordering Information

Part No.	Description
UT67001E	Probe Set, UniSTAC-II TX49-J-UT-E with USB/LAN interface, Watchpoint® TX49 debugger for Windows®
UT67000E	Probe Set, UniSTAC-II TX49-J-U-E with USB interface, Watchpoint® TX49 debugger for Windows®

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