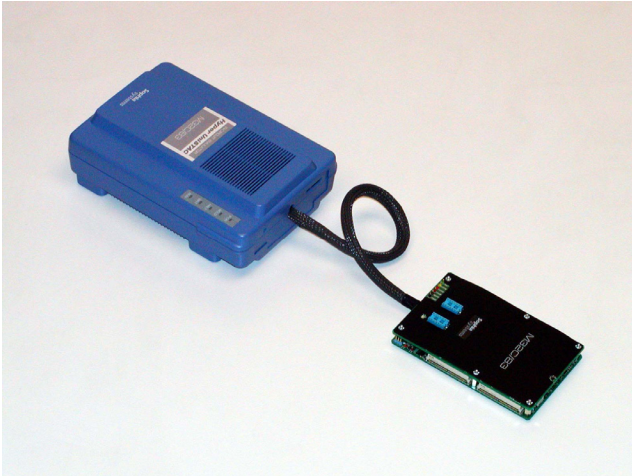


# HyperSTAC™ M16C/805 In-Circuit Emulator



- ❑ Supports Renesas Technology M16C/805 microcontrollers
- ❑ Real-time execution with 0 wait state up to 20 MHz in target system
- ❑ Optional probe end adapters are available to support different MCU packages
- ❑ Internal access trace capability
- ❑ Unlimited software breakpoints
- ❑ Flash ROM download capability
- ❑ View/modify internal peripheral register
- ❑ USB and LAN host interfaces supported for high-speed communication with host computer
- ❑ Watchpoint®, a powerful high-level language debugger for Windows®, provides common user interface for all Sophia Systems emulators  
(USB: Windows98/Me/2000/XP)  
(TCP/IP: Windows98/Me/NT/2000/XP)
- ❑ IBM PC/AT or compatible, NEC PC98, and Notebook PC

## Specifications

<b>Target CPU</b>	M30803/805 series
<b>Emulator CPU</b>	M30805SGP (256K bytes internal ROM, 24K bytes internal RAM)
<b>Memory and I/O</b>	Minimal space is required for monitor area
<b>Packages</b>	100P6S-A (100-pin, 0.65mm) 100P6Q-A (100-pin, 0.5mm) 144P6Q-A (144-pin, 0.5mm)
<b>Interrupts</b>	Both internal and external interrupts are available for real-time MCU execution.
<b>CPU Clock</b>	Maximum 20 MHz
<b>Target Voltage</b>	5V to 3.3 V
<b>Breakpoints and Break options</b>	Execution address break options: <ul style="list-style-type: none"> <li>• 3 hardware breakpoints (execution address break)</li> <li>• Unlimited software breakpoints</li> </ul> Other break options: <ul style="list-style-type: none"> <li>• Forced break from Debug Monitor</li> <li>• Break on Write-protect error</li> <li>• Break on Access-protect error</li> <li>• Break on Trace-End</li> </ul>
<b>Reset</b>	Reset the MCU during program execution
<b>ICE Environment Setting</b>	Enable/disable bus cycle time-out Enable/disable target control signals: RES-, NMI
<b>Emulation Memory</b>	Internal ROM 256K, internal RAM (using CPU internal resources), 4 Mbytes emulation memory as standard

<b>Real-time Trace</b>	-64K clock cycles branch trace storage capacity -Store trace address, data, status, external control signal <b>Real-time Trace Mode:</b> <b>Free run-</b> Continuously records Trace data <b>And-</b> Triggers when all of the points are satisfied (any sequence) <b>Or-</b> Triggers when one of the points is satisfied. <b>Sequential-</b> Triggers on sequential levels <b>Sampling-</b> Records specific trigger cycles <b>Trigger Point Conditions:</b> The following conditions can be specified when defining trigger points: <b>Address:</b> Specify a memory address or address range. (Maskable) <b>Data:</b> Specify a data value (Maskable) <b>CPU Status:</b> Specify (CPU signals, External trigger).
<b>Flash Memory</b>	Download to flash memory on the target system
<b>Bus Monitor</b>	Monitor a specific block of memory data bus in real-time
<b>Coverage Performance Analyzer</b>	The following measurements can be performed <b>Profile:</b> Measures the frequency of the program cycle, or the module cycle within the respected address spaces. <b>Coverage:</b> Measures the ratio of instruction execution, access data address spaces. <b>Performance:</b> Measures the time of the program execution cycles, the function execution time.

# Configuration

The HyperSTAC is the latest state of the art emulator, which connects directly to your PC/AT or notebook computer and provides a high level of software debugging for embedded system development.

## Hardware

The HyperSTAC M16C/805 Emulation System consists of the M16C/805 Probe Set and with USB host interface or LAN interface as an option.

## Software

**Watchpoint®**, a high-level language debugger for Windows®, is included with the M16C/805 Probe Set.

### Media:

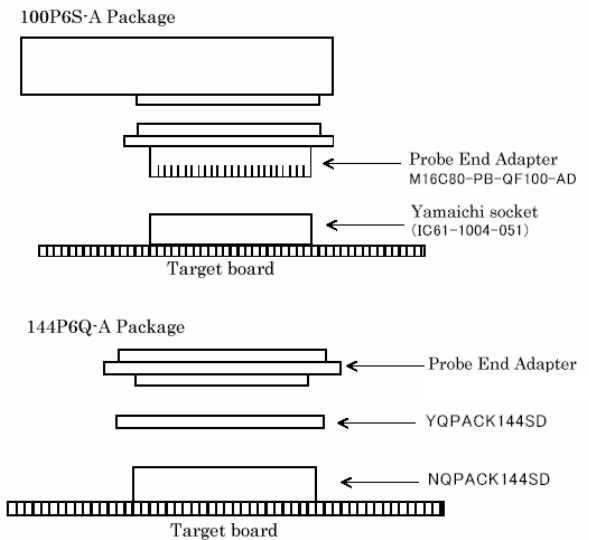
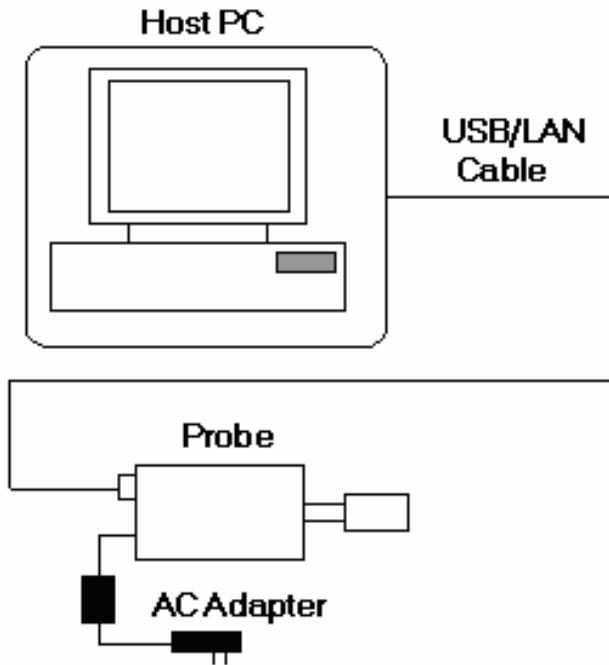
CD-ROM

### Supported Tool Chains:

Watchpoint supports the following compiler and assemblers:

Renesas M16C/80 series compiler

## Target Connection



## Ordering Information

Part No.	Description
HY61000E	Probe Set, HyperSTAC M16C/805 with USB interface, and Watchpoint® M16C/805 debugger
HY61001E	Probe Set, HyperSTAC M16C/805 with USB & LAN interface, and Watchpoint® M16C/805 debugger
<b>Option</b>	
CS2242E	Extension Cable SH704x

### Probe End Adapters

Support Package	Part Number	Description
100P6S-A (100-pin, 0.65mm)	CS2253A	M16C80-PB-QF100-AD
	Yamaichi socket	IC61-1004-051
100P6Q-A (100-pin, 0.5mm)	CS22650A	M16C62P-PB-QF100S-AD
	CS2231B	YQPACK100SD
	CS2231C	NQPACK100SD
144P6Q-A (144-pin, 0.5mm)	CS2253D	M16C80-PB-QF144-AD
	CS2246B	YQPACK144RD
	CS2246C	NQPACK144RD

Minimum Host System requirements for HyperSTAC/Watchpoint M16C/62P Debugger:

**OS:** <USB>Windows98/Me/2000/XP  
<TCP/IP>Windows98/Me/NT/2000/XP  
**Memory:** 32 MB (64 MB recommended)  
**Hard Disk:** 20 MB

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